


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Hideo Nakaya, et al.
Serial No. : 10/762,996
Filed : January 22, 2004
For : Integrated Circuit And Information Signal Processing Apparatus
Having Multiple Processing Portions (As Amended)
Examiner : Kostak, Victor R.
Art Unit : 2622
Confirmation No. : 6199

745 Fifth Avenue
New York, NY 10151
(212) 588-0800

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being transmitted via
Electronic Filing Services on November 13, 2007

DeAndre Breeland
(Name of person signing transmittal)

Signature
November 13, 2007
Date of Signature

AMENDMENT UNDER 37 C.F.R. §1.121

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Non-Final Office Action mailed on August 10, 2007, having a three-month period for response set to expire on November 13, 2007 (November 10, 2007 being a Saturday and November 12, 2007 being a federal holiday), please amend the above-identified applications as follows.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the Listing of Claims which begins
on page 5 of this paper.

Remarks/Arguments begin on page 14 of this paper.